



31-10-05

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Attorney Docket No.: 254-094-PCT2/C1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Title: Memory Expansion and Integrated Circuit Stacking System and Method
Application No.: 10/804,452
Filing Date: March 31, 2004
Inventor: Russel Rapport, et al.
Assignee: Staktek Group L.P.
Examiner: TBA
Art Unit: 2816

**FIRST SUPPLEMENTAL
INFORMATION DISCLOSURE STATEMENT**

Mail Stop - AMENDMENT
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Pursuant to the guidelines for Information Disclosure Statements, 37 C.F.R. §§ 1.97-1.98, attached hereto are Forms PTO 1449 (2 sheets) with references listed for consideration by the Office.

Pursuant to Official Gazette Notice No. 14, dated August 5, 2003, copies of the listed written U.S. references are not enclosed. However, in accordance with 37 C.F.R. § 1.98(a)(2), copies of the listed written foreign references are being provided.

In accordance with 37 C.F.R. §§ 1.97(g)-(h), the filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made or an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b) or prior art under 35 U.S.C. §§ 102 or 103.

This Information Disclosure Statement is being filed before receipt of the first Office action, so no fee is believed to be due.



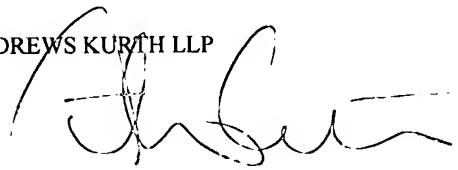
The examiner should also be aware of the co-pending applications in the same application family as the above-referenced application. The co-pending applications are: App. No.: 10/400,309 filed 3/27/03; App. No.: 10/136,890 filed 5/02/02; App. No.: 10/435,192 filed on 5/09/03; App. No.: 10/453,398 filed on 6/03/03; App. No.: 10/457,608 filed on 6/09/03; App. No.: 10/631,886 filed on 7/11/03; App. No.: 10/624,097 filed on 7/21/03; App. No.: 10/804,452 filed on 3/19/04; App. No.: 10/814,530 filed on 3/31/04; App. No.: 10/814,532 filed on 3/31/04; App. No.: 10/828,495 filed on 4/20/04; App. No.: 10/836,855 filed on 4/30/04; App. No.: 10/845,029 filed on 5/13/04; App. No.: 10/709,732 filed on 5/25/04; App. No.: 10/872,897 filed on 6/21/04; App. No.: 10/873,847 filed on 6/22/04; App. No.: 10/913,220 filed on 8/06/04; App. No.: 10/914,483 filed on 8/09/04; App. No.: 10/917,216 filed on 8/12/04; App. No.: 10/931,828 filed on 9/01/04; App. No.: 10/958,584 filed on 10/05/04; App. No.: 10/958,924 filed on 10/05/04; App. No.: 10/958,934 filed on 10/05/04; App. No.: 10/963,867 filed on 10/12/04; App. No.: 10/978,149 filed on 10/29/04; App. No.: 11/011,469 filed on 12/14/04.

The Commissioner is hereby authorized to charge any fees which may be required in connection with the filing, or to credit any overpayment to Deposit Account No. 50-0897, upon which account the undersigned has the authority to sign. **A duplicate copy of Forms PTO 1449 are enclosed.**

Respectfully submitted,

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By:


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1/7/05

January 7, 2005

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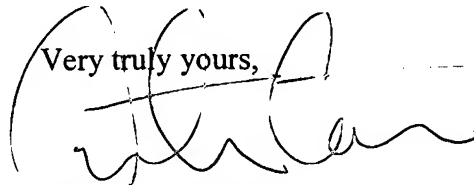
RE: Application No.: 10/804,452
 Filing Date: March 19, 2004
 Title: Memory Expansion and Integrated Circuit Stacking System
 and Method
 Docket No.: 254-094-PCT2/C1

Dear Commissioner:

Please find enclosed for filing the following:

1. First Supplemental Information Disclosure Statement (2 pages);
3. Forms PTO 1449 (2 pages) (with duplicate copy);
4. 7 references; and
5. a return postcard for confirmation of receipt.

The Commissioner is hereby authorized to charge any fees which may be required to Deposit Account 50-0897. Please return the postcard confirming your receipt of the enclosed materials.

Very truly yours,


Nathan H. Calvert

NHC/at
Enclosures

Form PTO-1449 (Modified)
(Rev. 11/92)U.S. Dept. of Commerce
Patent and Trademark Office

ATTY. DOCKET NO.

SERIAL NO.

254-094-PCT-2-C1

10/804,452

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)

APPLICANT

Russell Rapport, et al.

FILING DATE

March 19, 2004

GROUP

2816

(37 CFR 1.98(b))

U.S. PATENT DOCUMENTS

*Examiner Initial	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date (if appropriate)
	US 5,117,282	05/26/1992	Salatino	357	81	10/29/1990
	US 5,448,511	09/05/1995	Paurus, et al.	365	52	06/01/1994
	US 5,642,055	06/24/1997	Difrancesco	324	757	05/12/1995
	US 5,917,709	06/29/1999	Johnson, et al.	361	803	06/12/1997
	US 6,121,676	09/19/2000	Solberg	257	686	12/11/1997
	US 6,205,654 B1	03/27/2001	Burns	29	830	12/28/1998
	US 6,360,433 B1	03/26/2003	Ross	29	840	09/19/2000
	US 6,376,769 B1	04/23/2002	Chung	174	522	03/14/2000

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	2003309246(A)	10/31/2003	Japan			Partial	
	2003086761(A)	3/20/2003	Japan			Partial	
	2003086760(A)	3/20/2003	Japan			Partial	
	2003037246(A)	2/7/2003	Japan			Partial	
	2001332683(A)	11/30/2001	Japan			Partial	
	2001085592(A)	3/30/2001	Japan			Partial	
	2001077294(A)	3/23/2001	Japan			Partial	

OTHER DOCUMENTS

(INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-1449 (Modified) (Rev. 11/92)				U.S. Dept. of Commerce Patent and Trademark Office		ATTY. DOCKET NO. 254-094-PCT-2-C1		SERIAL NO. 10/804,452	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)									
(37 CFR 1.98(b))									
U.S. PATENT DOCUMENTS									
*Examiner Initial	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date (if appropriate)			
	US 6,552,910 B1	04/22/2003	Moon, et al.	361	749	06/28/2000			
	US 6,627,984 B2	09/30/2003	Bruce, et al.	257	686	07/24/2001			
	US 6,660,561 B2	12/09/2003	Forthun	438	109	07/24/2002			
	US 6,677,670 B2	01/13/2004	Kondo	257	686	04/25/2001			
	2004/0150107 A1	08/05/2004	Cha, et al.	257	738	12/29/2003			
	2004/0021211 A1	02/05/2004	Damberg	257	282	09/06/2002			
	2003/0168725 A1	09/11/2003	Warner et al.	257	686	10/28/2002			
	2001/0040793 A1	10/15/2001	Inaba	361	749	01/31/2001			
FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION									
	Document Number	Date	Country	Class	Subclass	Translation			
						Yes	No		
OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)									
EXAMINER				DATE CONSIDERED					
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.									